

10/581674

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Application Data Sheet

Application Information

Application number::

Filing Date::

Application Type::

Regular

Subject Matter::

Utility

Title ::

METHOD FOR
MANUFACTURING THIN
FILM INTEGRATED CIRCUIT
DEVICE, NONCONTACT
THIN FILM INTEGRATED
CIRCUIT DEVICE AND
METHOD FOR
MANUFACTURING THE
SAME, AND IDTAG AND
COIN INCLUDING THE
NONCONTACT THIN FILM
INTEGRATED CIRCUIT
DEVICE

Attorney Docket Number::

0756-7719

Total Drawing Sheets::

23

Small Entity?::

No

Applicant Information

Applicant Authority Type::

Inventor

Primary Citizenship Country::

Japan

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Shunpei

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Applicant Authority Type:: Inventor
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Applicant Authority Type:: Inventor
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Correspondence Information

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Representative Information

Representative Customer Number:: 31780

Domestic Priority Information

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	National Stage	PCT/JP2004/018978	12/14/2004

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
Japan	2003-417317	12/15/2003	Yes

Assignee Information

Assignee name:: Semiconductor Energy Laboratory Co., Ltd.
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